Mon Nov 28, 1988 DANIEL T. PIHULIC Change_Screen Interrupt Hold/Resume Clear_0 ut | Input_Ref | NDC_Addition | Pa Scroll_Mode Move Text Search Close

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2,581,715

Apr. 8, 1986 L1: 1 of 1

Fourier transform processor

INVENTOR: APPL-NO: DATE FILED: Gilbert P. Hyatt, P.O. Box 4584, Anaheim, CA 92803

6/425,731 Sep. 28, 1982

REL-US-DATA:

lep. 28, 1982
ontinuation—in—part of Ser. No. 160,872, Jun. 19, 1980, Pat. No. 4,491,930, Jan. 1, 1985, and a continuation—in—part of Ser. No. 860,257, Dec. 14, 1977, Pat. No. 4,371,923, Feb. 1, 1983, said Ser. No. 160,872 is a continuation—in—part of Ser. No. 889,301, Mar. 23, 1978, Pat. No. 4,322,819, Mar. 30, 1982, Ser. No. 860,278, Dec. 13, 1977, Pat. No. 4,471,385, Sep. 11, 1984, Ser. No. 849,812, Nov. 9, 1977, Ser. No. 844,765, Oct. 25, 1977, Pat. No. 4,523,290, Jun. 11, 1985, Ser. No. 812,285, Jul. 1, 1977, Pat. No. 4,371,953, Feb. 1, 1983, Ser. No. 801,879, May 13, 1977, Pat. No. 4,371,953, Feb. 1, 1983, Ser. No. 801,879, May 13, 1977, Pat. No. 4,144,582, Mar. 13, 1979, Ser. No. 752,240, Dec. 20, 1976, abandoned, Ser. No. 754,660, Dec. 27, 1976, Pat. No. 4,486,850, Dec. 4, 1984, Ser. No. 730,756, Oct. 7, 1976, abandoned, Ser. No. 727,307, Sep. 27, 1976, abandoned, Ser. No. 550,231, Feb. 14, 1975, Pat. No. 4,209,843, Jun. 24, 1980, Ser. No. 522,559, Nov. 11, 1974, Pat. No. 4,209,852, Jun. 24, 1980, Ser. No. 476,743, Jun. 5, 1974, Pat. No. 4,364,110, Dec. 14, 1982, Ser. No. 490,816, Jul. 22, 1974, Pat. No. 4,029,853, Jun. 24, 1980, Ser. No. 490,816, Jul. 22, 1974, Pat. No. 4,029,853, Jun. 24, 1980, Ser. No. 402,520, Oct. 1, 1973, Ser. No. 339,817, Mar. 9, 1973, Pat. No. 4,034,276, Jul. 5, 1977, Ser. No. 366,741, Jun. 4, 1973, Pat. No. 3,986,922, Oct. 12, 1976, Ser. No. 366,741, Jun. 4, 1973, Pat. No. 3,986,922, Oct. 12, 1976, Ser. No. 255,941, Jan. 22, 1973, Pat. No. 4,060,848, Nov. 29, 1977, Ser. No. 325,933, Jan. 22, 1973, Pat. No. 4,060,848, Nov. 29, 1977, Ser. No. 325,941, Jan. 22, 1973, Pat. No. 4,060,848, Nov. 29, 1977, Ser. No. 325,933, Jan. 22, 1973, Pat. No. 4,060,848, Nov. 29, 1977, Ser. No. 325,941, Jan. 22, 1973, Pat. No. 4,060,848, Nov. 29, 1977, Ser. No. 325,933, Jan. 22, 1973, Pat. No. 4,370,720, Jan. 25, 1983, Ser. No. 230,877, Nov. 1, 1972, Ser. No. 291,394, Sep. 22, 1972, Pat. No. 4,370,720, Jan. 25, 1983, Ser. No. 232,459, Mar. 7, 1972, Pat. No. 4,370,720, Jan. 25, 1983, Ser. No. 230,87 Continuation-in-part of Ser. No. 160,872, Jun. 19, 1980, Pat. 1971, Ser. No Dec. 28, 1970. [4] G06F 15*31

INT-CL:

364*726 US-CL-ISSUED: US-CL-CURRENT: 364*726

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PRIM-EXMR: Felix D. Gruber LEGAL-REP: Gilbert P. Hyatt

ABSTRACT:

An improved Fourier transform processor is provided for generating frequency domain output signals in response to time domain input signals. Various configurations are provided. Processing on the fly as samples are received yields improvements such as greater speed and reduced circuitry. Generating higher resolution output samples in response to lower resolution input samples yields improvements such greater precision and reduced circuitry. Directly combining complex signal components yields improvements such as greater SNR and reduced circuitry. Single bit processing yields improvements such as greater speed and reduced circuitry.
44 Claims, 50 Drawing Figures

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